



EV077335116US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/921,518
 Filing Date August 1, 2001
 Inventor John T. Moore
 Assignee Micron Technology, Inc.
 Group Art Unit 2818
 Examiner Phuc T. Dang
 Attorney's Docket No. MI22-1669
 Title: Method Of Forming Integrated Circuitry, Method Of Forming Memory Circuitry,
 And Method Of Forming Random Access Memory Circuitry

fee OK

9-12-02

#10/B

A. Spruell

SUPPLEMENTAL PRELIMINARY AMENDMENT
FOLLOWING AUGUST 1, 2002 RCE FILING

To: Assistant Commissioner for Patents
 Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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Subsequent to the RCE filing of August 1, 2002, and prior to an Office
 Action on the merits, Applicant amends and remarks as follows:

AMENDMENTS**In the Title:**

✓
 Please change the title to read as follows:

(changed in PALM)
 L.S.

Method Of Forming Integrated Circuitry, Method Of
 Forming Memory Circuitry, And Method Of Forming
 Random Access Memory Circuitry

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01 FC:103 756.00 OP
 02 FC:102 588.00 OP

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